

	Application No. Applicant(s)		
Notice of Allowability	09/708,795	SIDIROPOULOS ET	AI
	Examiner	Art Unit	
	Edith M. Chang	2611	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to <u>amendment filed March 2, 2006</u> .			
2. X The allowed claim(s) is/are 1,2,4-25,27-35,37-40 and 42-62.			
3.			
Attachment(s) 1. □ Notice of References Cited (PTO-892) 2. □ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. □ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 4. □ Examiner's Comment Regarding Requirement for Deposit of Biological Material 5. □ Notice of Informal Patent Application (PTO-152) 6. □ Interview Summary (PTO-413), Paper No./Mail Date 7. □ Examiner's Amendment/Comment 8. ☑ Examiner's Statement of Reasons for Allowance			

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DETAILED ACTION

Response to Arguments/Remarks

1. Applicant's arguments, see pages 18-20, filed March 02, 2006, with respect to claims 1-67 have been fully considered and are persuasive. The rejection of claims 1,2, 4-7, 9-14, 16-25, 27-33, 35, 37-40, 42-44, 46-50, and 52-53 has been withdrawn.

Allowable Subject Matter

- 2. Claims 1-2, 4-25, 27-35, 37-40, 42-62 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:

The prior art of record fails to teach or suggest, alone or in a combination, among other things, at least an integrated circuit apparatus and its method of pseudo-differential voltage signaling as a whole, the combination of elements and features, which includes a reference receiver/buffer is a unity gain amplifier to produce a buffered voltage that represents the difference between an undistributed voltage in part on a common voltage and a distributed voltage received by multiple signal receivers, or the reference receiver/buffer having bandwidth of at least ten times the resonant input frequency, wherein the coupled signal noise introduced in the pseudo-differential voltage signaling is canceled as recited in the claim.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edith M. Chang whose telephone number is 571-272-3041. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammed H. Ghayour can be reached on 571-272-3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Edith Chang March 22, 2006

KHAITRAN PRIMARY EXAMINER

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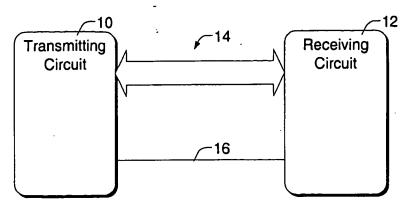


Fig. 1 Prior Art

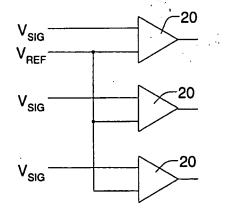
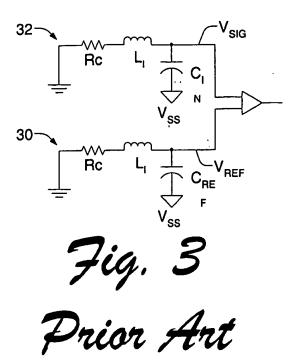
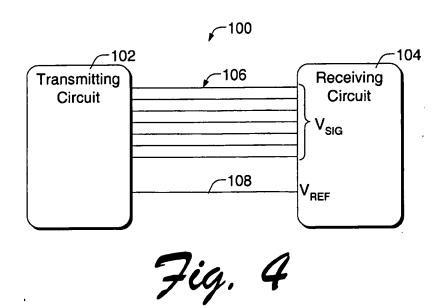


Fig. 2
Prior Art

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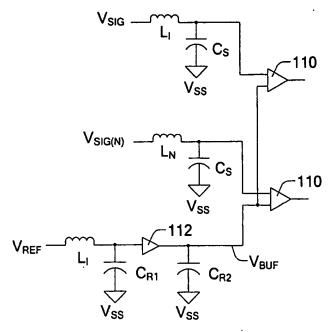


Fig. 5

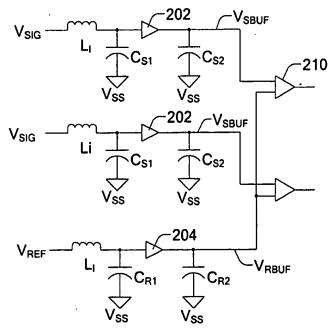


Fig. 6



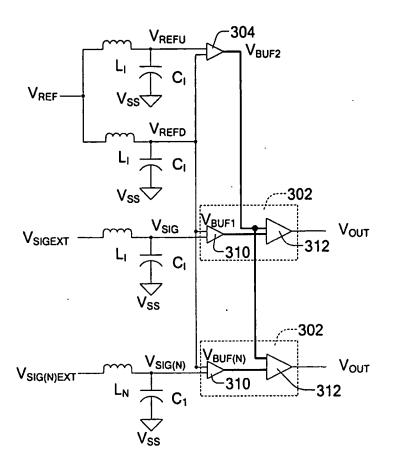


Fig. 7